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amount of work with fewer resources. I/O devices, including such popular peripherals as modems, printers and telephones, have generally remained relatively static. Various mechanisms have been proposed to accommodate I/O needs. For example, dedicated I/O controllers, usually referred to as input/output processors (IOPs), perform all operations necessary to convert communications with external I/O devices to the form handled by a processor. The IOP may include interrupt support and I/O device drivers to allow a user to write programs which use the I/O capabilities of the computer system. The IOP is connected to a bus which provides memory and processing access to the I/O data and control signals. Interrupts are usually hard-wired to interrupt pins on the IOP and allow the IOP to initiate a process performed by a processor and memory. The I/O processor generates an interrupt signal when a particular event occurs. One such event is when data is ready for the processor. Accordingly, the I/O processor may determine when data is ready by monitoring the state of input and output pins on the IOP. While dedicated IOPs have proven to be reliable and useful in many computer applications, they are relatively expensive. Furthermore, they do not function well in computer systems which use a shared memory for both I/O processing and program execution. The IOPs must therefore share the memory bus, which may prevent other devices from using the bus. To reduce the cost of I/O devices and to improve the I/O capabilities of computer systems, it has become desirable to put some of the I/O processing

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